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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of:

Ching-Chen Hao

Group Art Unit: 2811

Serial No.: 10/813,786

Examiner: Nguyen, Cuong Quang

Filed: March 30, 2004

In Response to Office Action

Dated: Dec. 14, 2004

For: Halogen Gettering Method for Forming Field Effect Transistor
(FET) Device

Attorney Docket No.: 67,200-1071 (2002-0834)

Certificate of Mailing

I hereby certify that this paper is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: Jan. 7, 2005

[Signature]
Kathy Dixon

RESPONSE TO OFFICE ACTION

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

In response to a restriction requirement imposed by the Examiner, the Applicants hereby elect with traverse the prosecution of Group II, method claims 1-18 for prosecution. Claims 19-20 are hereby cancelled and withdrawn from further consideration.